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EE 313: Basic Electrical Engineering I

Prepared by Brian Manhire, Ph.D.
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*Stocker Center, home of Ohio University's
Russ College of Engineering & Technology*

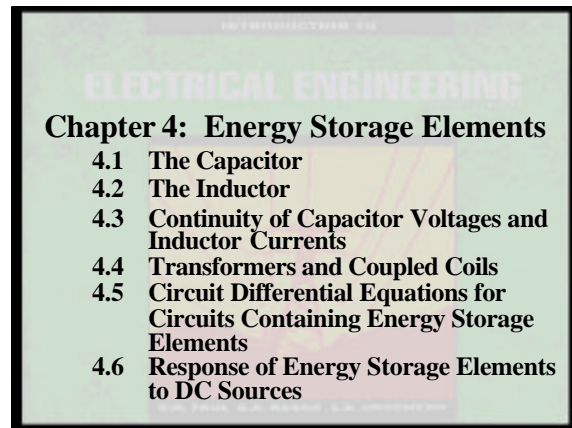
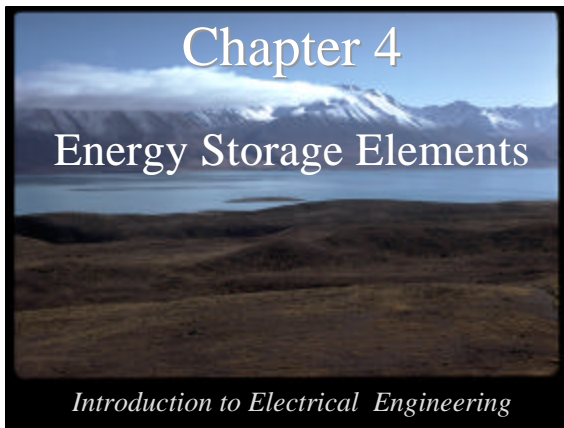
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For Part 1 of

**Introduction to Electrical
Engineering, 2/e**

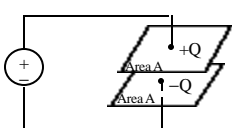
by **C.R. Paul, S.A. Nasar
and L.E. Unnewehr**

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4.1 Energy Storage Elements: The Capacitor

- Voltage V (in Volts) applied to the conceptual capacitor shown causes a charge Q in Coulombs to be deposited on the top plate
- The top plate's charge pushes an equal amount of (displacement) charge off of the bottom plate, leaving a negative (absence of) charge $-Q$ on the bottom plate
- The relationship between the applied voltage and charge is $Q = CV$ (C is in Farads when V is in Volts and Q is in Coulombs; i.e., $1 \text{ F} = 1 \text{ Coulomb per Volt}$)



$$Q = CV$$

$$C = 8.84 \times 10^{-12} \text{ (A/d)}$$

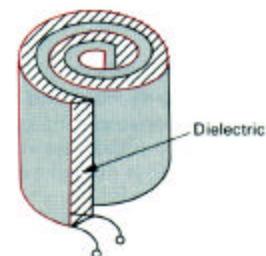
Energy is stored in the electric field existing between the plates

8:44 PM

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4.1 Energy Storage Elements: The Capacitor

FIGURE 4.2
Physical construction of a capacitor to occupy a small space.



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4.1 The Capacitor cont.

- $Q(t) = Cv(t)$, where C is constant
- Differentiate both sides with respect to time to obtain $dQ(t)/dt = C \times dv(t)/dt$
- However, $dQ(t)/dt = i(t)$ (current) \therefore
- $i(t) = C \times dv(t)/dt$, where the passive sign convention is built into the formula (see below)

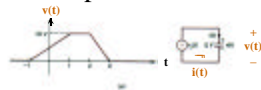
$$\begin{aligned}
 \downarrow i(t) &= C \frac{dv(t)}{dt} \quad \text{and} \\
 \begin{array}{c} + \\ | \\ C \\ | \\ - \end{array} v(t) &= \frac{1}{C} \int_{-\infty}^t i(\tau) d\tau = \frac{1}{C} \int_{-\infty}^{t_0} i(\tau) d\tau + \frac{1}{C} \int_{t_0}^t i(\tau) d\tau \\
 &= v(t_0) + \frac{1}{C} \int_{t_0}^t i(\tau) d\tau
 \end{aligned}$$

4.1 The Capacitor cont.

- **Stored energy = $W(t) = CV^2/2$** (see below)

$$\begin{aligned}
 W(t) &= \int_{-\infty}^t p(\tau) d\tau = \int_{-\infty}^t v(\tau) i(\tau) d\tau \\
 &= \int_{-\infty}^t v(\tau) C \frac{dv(\tau)}{d\tau} d\tau \\
 &= C \int_{v(-\infty)}^{v(t)} v(\tau) dv(\tau) = \frac{1}{2} Cv(t)^2 - \frac{1}{2} Cv(-\infty)^2 \\
 \therefore W(t) &= \frac{1}{2} Cv(t)^2 \quad (\text{Assuming no initial stored energy})
 \end{aligned}$$

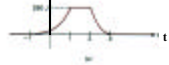
4.1 The Capacitor cont.



$$i(t) = C \frac{dv(t)}{dt}, C = 5 \text{ F}$$



$$w(t) = C v^2(t)/2$$



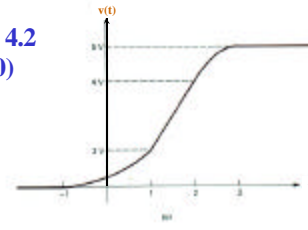
Text Example 4.1
(pp. 108-109)

4.1 The Capacitor cont.



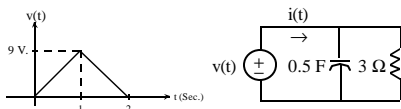
Text Example 4.2
(pp. 109-110)

FIGURE 4.2
Example 4.2:
Illustration of a
capacitor driven
by a current
source



4.1 The Capacitor cont.

Example 4-1

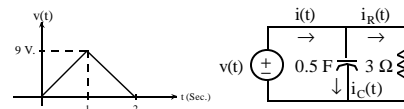


Sketch the source current $i(t)$ and calculate the following:

- The capacitor's maximum stored energy
- The total energy consumed by the resistor
- The total energy supplied by the source
- The total charge that flows through the resistor
- The resistor's average voltage

4.1 The Capacitor cont.

Example 4-1 cont.



$$v(t) = \begin{cases} 9 & V., 0 \leq t \leq 1 \text{ sec.} \\ -9t + 18 & V., 1 \leq t \leq 2 \text{ sec.} \\ 0 & V., \text{ Elsewhere} \end{cases} \quad i_R(t) = v(t)/3\Omega \text{ and } i_C(t) = 0.5F \times dv(t)/dt$$

Therefore

$$i_R(t) = \begin{cases} 3 & A., 0 \leq t \leq 1 \text{ sec.} \\ -3t + 6 & A., 1 \leq t \leq 2 \text{ sec.} \\ 0 & A., \text{ Elsewhere} \end{cases} \quad \text{and} \quad i_C(t) = \begin{cases} +4.5 & A., 0 < t < 1 \text{ sec.} \\ -4.5 & A., 1 < t < 2 \text{ sec.} \\ 0 & A., \text{ Elsewhere} \end{cases}$$

and **KCL** yields: $i(t) = i_R(t) + i_C(t)$

4.1 The Capacitor cont.

Example 4-1 cont.

$$i_g(t) = \begin{cases} 3t & \text{A., } 0 \leq t \leq 1 \text{ sec.} \\ -3t + 6 & \text{A., } 1 \leq t \leq 2 \text{ sec.} \\ 0 & \text{A., Elsewhere} \end{cases}$$

$$i_c(t) = \begin{cases} +4.5 \text{ A.,} & 0 < t < 1 \text{ sec.} \\ -4.5 \text{ A.,} & 1 < t < 2 \text{ sec.} \\ 0 & \text{A., Elsewhere} \end{cases}$$

$$i(t) = \begin{cases} 3t + 4.5 \text{ A.,} & 0 < t < 1 \text{ sec.} \\ -3t + 1.5 \text{ A.,} & 1 < t < 2 \text{ sec.} \\ 0 & \text{A., Elsewhere} \end{cases}$$

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4.1 The Capacitor cont.

Example 4-1 cont.

$$W_C = \frac{1}{2} C \times v^2$$

So the capacitor's maximum stored energy is $0.5 \times C \times (v_{\max})^2 = 0.5 \times 0.5 \text{ F} \times (9 \text{ V.})^2 = 20.25 \text{ J.}$

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4.1 The Capacitor cont.

Example 4-1 cont.

$$W_R = \int_{-\infty}^{\infty} P_R(t) dt = \int_0^2 v(t)^2 / 3\Omega dt$$

$$= \int_0^1 (9t \text{ V.})^2 / 3\Omega dt + \int_1^2 (-9t + 18 \text{ V.})^2 / 3\Omega dt = 18 \text{ J.}$$

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4.1 The Capacitor cont.

Example 4-1 cont.

The capacitor can neither consume nor create energy—it can only store energy

The voltage is nil for $0 \geq t \geq 2$ seconds so there is no net stored energy in the capacitor over the time interval $0 \leq t \leq 2$ seconds

Therefore it follows that the energy supplied (produced) by the source equals the energy consumed by the resistor (which is 18 J.)

What is the temporal behavior of the capacitor's stored energy?

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4.1 The Capacitor cont.

Example 4-1 cont.

$$Q_R = \int_{-\infty}^{\infty} i_R(t) dt = \int_0^2 v(t) / 3\Omega dt$$

$$= \int_0^1 (3t \text{ A.}) dt + \int_1^2 (-3t + 6 \text{ A.}) dt = 3 \text{ C.}$$

and $V_{\text{ave.}} = W_R / Q_R = 18 \text{ J.} / 3 \text{ C.} = 6 \text{ J./C.} = 6 \text{ V.}$

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4.1 The Capacitor cont.

- Capacitance in parallel adds:
 $C_{\text{tot}} = C_1 + C_2 + \dots + C_n$

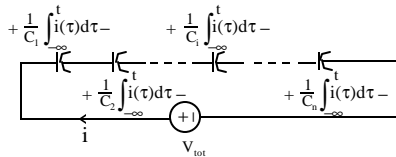
$$\text{KCL: } i_{\text{tot}} = \sum_{j=1}^{j=n} C_j \frac{dV}{dt} = \frac{dV}{dt} \sum_{j=1}^{j=n} C_j = C_{\text{tot}} \frac{dV}{dt}$$

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4.1 The Capacitor cont.

- Capacitance in series adds reciprocally:

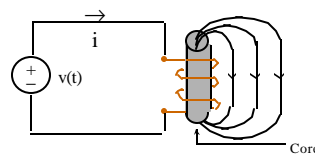
$$C_{\text{tot}}^{-1} = C_1^{-1} + C_2^{-1} + \dots + C_n^{-1}$$



$$\text{KVL: } v_{\text{tot}} = \sum_{j=1}^n \frac{1}{C_j} \int_{-\infty}^t i(\tau) d\tau = \frac{1}{C_{\text{tot}}} \int_{-\infty}^t i(\tau) d\tau$$

4.2 Energy Storage Elements: The Inductor

- Energy is stored in the magnetic field created by the current flowing in the coil (wrapped around a Ferromagnetic core)



$$\Psi = Li$$

$$v(t) = \frac{d\Psi}{dt}$$

Energy is stored in the magnetic field near the core

4.2 The Inductor cont.

- $\psi = Li(t)$, where L is constant
- Differentiate both sides with respect to time to obtain $d\psi(t)/dt = L \times di(t)/dt$
- However, $d\psi(t)/dt = v(t)$ (voltage) \therefore
- $v(t) = L \times di(t)/dt$, where the passive sign convention is built into the formula (see below)

$$L \frac{di(t)}{dt} = v(t) \quad \text{and}$$

$$i(t) = \frac{1}{L} \int_{-\infty}^t v(\tau) d\tau = \frac{1}{L} \int_{-\infty}^{t_0} v(\tau) d\tau + \frac{1}{L} \int_{t_0}^t v(\tau) d\tau$$

$$= i(t_0) + \frac{1}{L} \int_{t_0}^t v(\tau) d\tau$$

4.2 The Inductor cont.

- Stored energy = $W(t) = Li^2/2$ (see below)

$$W(t) = \int_{-\infty}^t p(\tau) d\tau = \int_{-\infty}^t v(\tau) i(\tau) d\tau$$

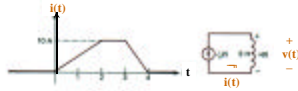
$$= \int_{-\infty}^t L \frac{di(\tau)}{d\tau} i(\tau) d\tau$$

$$= L \int_{i(-\infty)}^{i(t)} i(\tau) di(\tau) = \frac{1}{2} Li(t)^2 - \frac{1}{2} Li(-\infty)^2$$

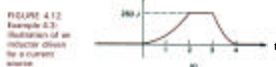
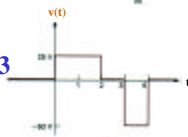
$$\therefore$$

$$W(t) = \frac{1}{2} Li(t)^2 \quad (\text{Assuming no initial stored energy})$$

4.2 The Inductor cont.



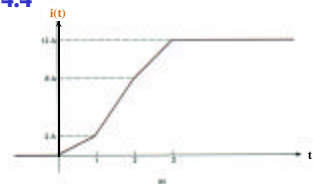
Text Example 4.3 (p. 117)



4.2 The Inductor cont.



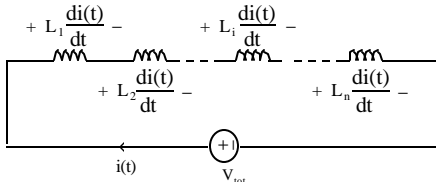
Text Example 4.4 (p. 118)



4.2 The Inductor cont.

- Inductance in series adds:

$$L_{\text{tot}} = L_1 + L_2 + \dots + L_n$$

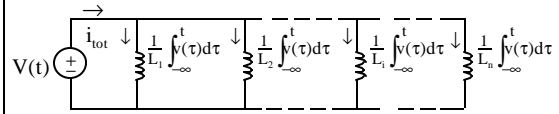


$$\text{KVL: } V_{\text{tot}} = \sum_{j=1}^{j=n} L_j \frac{di(t)}{dt} = L_{\text{tot}} \frac{di(t)}{dt}$$

4.2 The Inductor cont.

- Inductance in parallel adds reciprocally:

$$L_{\text{tot}}^{-1} = L_1^{-1} + L_2^{-1} + \dots + L_n^{-1}$$



$$\text{KCL: } i_{\text{tot}} = \sum_{j=1}^{j=n} \frac{1}{L_j} \int_{-\infty}^t v(\tau) d\tau = \frac{1}{L_{\text{tot}}} \int_{-\infty}^t v(\tau) d\tau$$

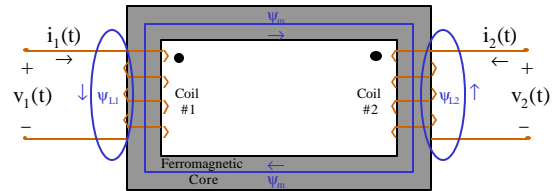
4.3 Continuity of Capacitor Voltage and Inductor Current

$$C \begin{matrix} \downarrow \\ i(t) \\ \uparrow \\ v(t) \end{matrix} \quad \begin{matrix} \text{Finite } i(t) \text{ requires finite } \frac{dv(t)}{dt} \\ \text{Finite } \frac{dv(t)}{dt} \text{ requires continuous } v(t) \end{matrix}$$

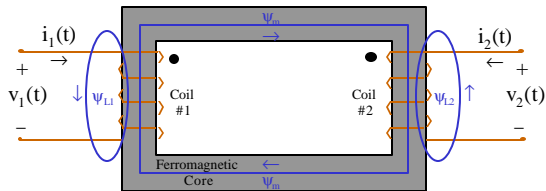
$$L \begin{matrix} \downarrow \\ i(t) \\ \uparrow \\ v(t) \end{matrix} \quad \begin{matrix} \text{Finite } v(t) \text{ requires finite } \frac{di(t)}{dt} \\ \text{Finite } \frac{di(t)}{dt} \text{ requires continuous } i(t) \end{matrix}$$

4.4 Transformers and Coupled Coils

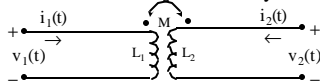
- Coil currents produce two types of magnetic flux (leakage & mutual)
- The (black) "dot" convention is used to indicate the configuration of how each coil is wound on the core



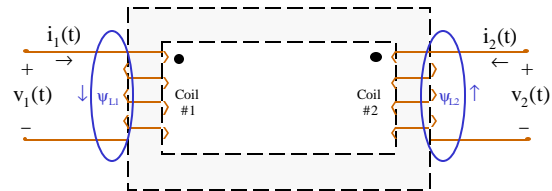
4.4 Transformers and Coupled Coils cont.



The coil-core arrangement shown (physically) above is shown schematically below



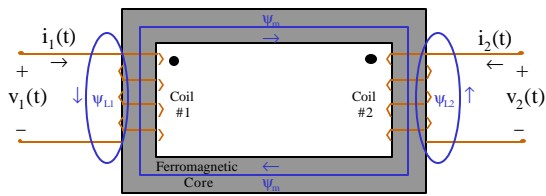
4.4 Transformers and Coupled Coils cont.



Without mutual induction, the coils are merely two independent inductors having the *decoupled* equations given below

$$v_1(t) = L_1 \frac{di_1(t)}{dt} \quad \text{and} \quad v_2(t) = L_2 \frac{di_2(t)}{dt}$$

4.4 Transformers and Coupled Coils cont.

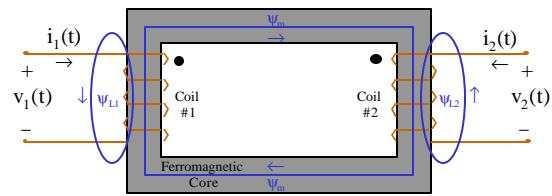


With mutual induction, the coils become dependent inductors having the *coupled* equations given below

$$v_1(t) = L_1 \frac{di_1(t)}{dt} + M \frac{di_2(t)}{dt} \quad \text{and} \quad v_2(t) = L_2 \frac{di_2(t)}{dt} + M \frac{di_1(t)}{dt}$$

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4.4 Transformers and Coupled Coils cont.



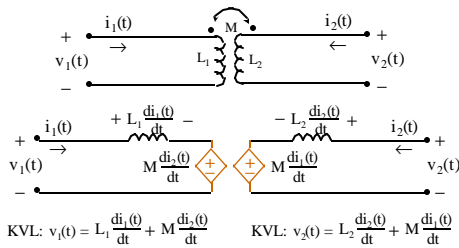
Each term in the KVLs below is a voltage

$$v_1(t) = L_1 \frac{di_1(t)}{dt} + M \frac{di_2(t)}{dt} \quad \text{and} \quad v_2(t) = L_2 \frac{di_2(t)}{dt} + M \frac{di_1(t)}{dt}$$

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4.4 Transformers and Coupled Coils cont.

The voltage induced in each coil by the other coil's field can be represented using *dependent* voltage sources as shown below

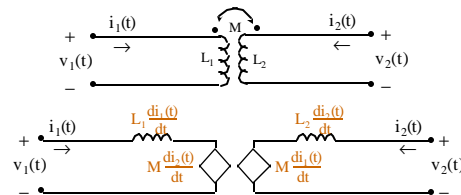


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4.4 Transformers and Coupled Coils cont.

The lumped-circuit model can be developed in stages as follows:

Step 1: Draw the circuit showing self- and mutual-inductance voltages *without* their polarities (see below)

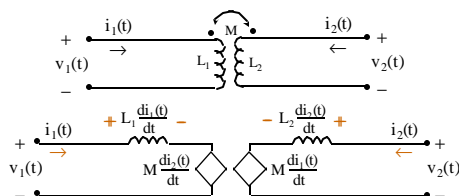


N.B.: The terminal voltages and currents *are* included in this first step's schematic but the polarities of the four voltages associated with the transformer *aren't*

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4.4 Transformers and Coupled Coils cont.

Step 2: Assign self-inductance voltage polarities using the passive sign convention in conjunction with the directions of $i_1(t)$ and $i_2(t)$

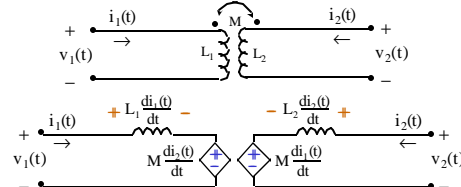


N.B.: $i_1(t)$ flows into the positive sign of the $L_1(di_1(t)/dt)$ voltage drop and $i_2(t)$ flows into the positive sign of the $L_2(di_2(t)/dt)$ voltage drop

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4.4 Transformers and Coupled Coils cont.

Step 3: Assign the (mutual inductance) dependent voltage source polarities using the rule: "If *both* currents flow *into* the dots or if *both* currents flow *out of* the dots, then the mutual voltages *aid (boost)* the self-inductance voltage drops—otherwise they *oppose (buck)*"

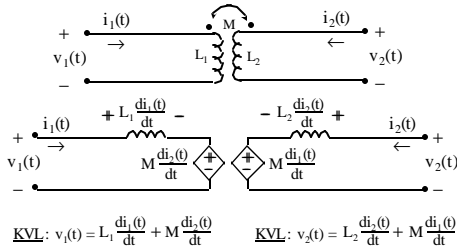


N.B.: Here, *aid (boost)* applies since *both* currents flow from the terminals (ports) *into* transformer dots

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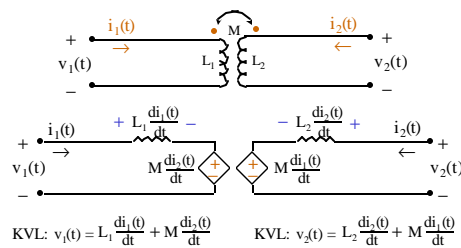
4.4 Transformers and Coupled Coils cont.

The transformer, its corresponding lumped-circuit (dependent-sources based) model and associated KVLs are shown below



4.4 Transformers and Coupled Coils cont.

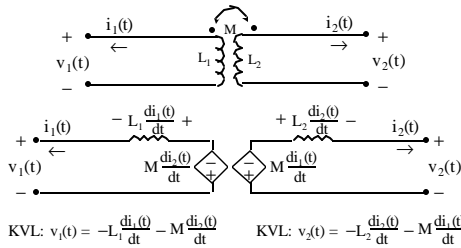
Dot-Voltage Polarity Rule: If both currents flow into the dots or if both currents flow out of the dots, then the mutual voltages aid (boost) the self-inductance voltage drops—otherwise they oppose (buck)



N.B.: The polarity of each self-inductance voltage drop is determined by the directions of $i_1(t)$ and $i_2(t)$ using the passive sign convention

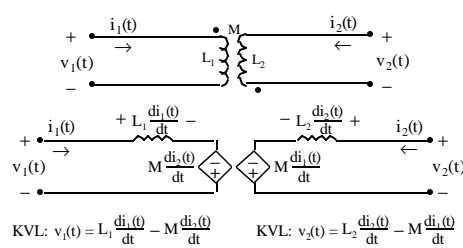
4.4 Transformers and Coupled Coils cont.

If both currents flow into the dots or both currents flow out of the dots then the mutual voltages aid the self-inductance voltage drops—otherwise they oppose (see example #1 below)



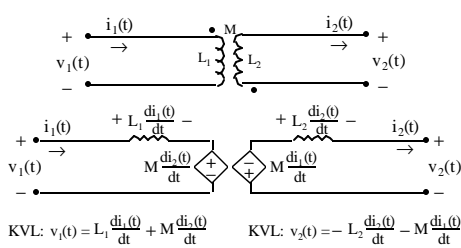
4.4 Transformers and Coupled Coils cont.

If both currents flow into the dots or both currents flow out of the dots then the mutual voltages aid the self-inductance voltage drops—otherwise they oppose (see example #2 below)



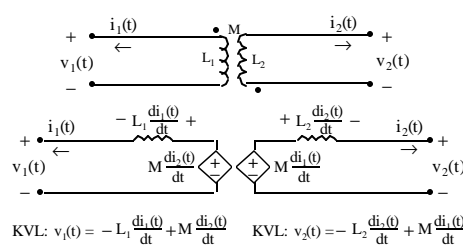
4.4 Transformers and Coupled Coils cont.

If both currents flow into the dots or both currents flow out of the dots then the mutual voltages aid the self-inductance voltage drops—otherwise they oppose (see example #3 below)



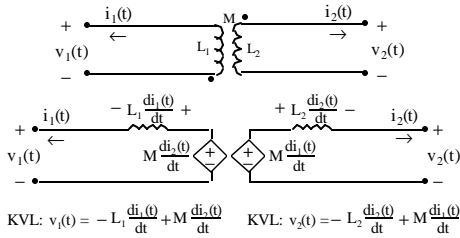
4.4 Transformers and Coupled Coils cont.

If both currents flow into the dots or both currents flow out of the dots then the mutual voltages aid the self-inductance voltage drops—otherwise they oppose (see example #4 below)



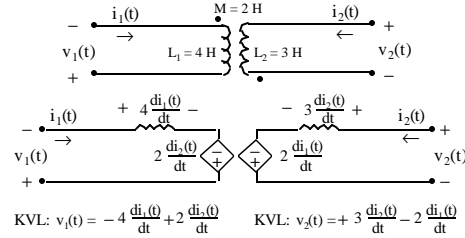
4.4 Transformers and Coupled Coils cont.

If *both* currents flow *into* the dots or *both* currents flow *out* of the dots then the mutual voltages *aid* the self-inductance voltage drops—otherwise they *oppose* (see *example #5* below)



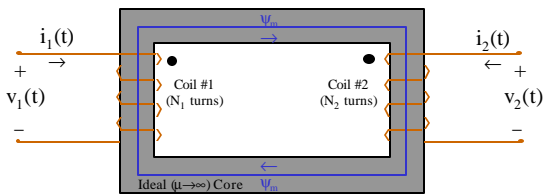
4.4 Transformers and Coupled Coils cont.

If *both* currents flow *into* the dots or *both* currents flow *out* of the dots then the mutual voltages *aid* the self-inductance voltage drops—otherwise they *oppose* (see *text problem 4.22* (p. 141) below)

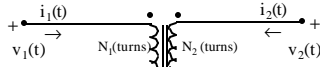


N.B.: The dependent source technique also applies to cases of three or more coils

4.4 Ideal Transformer



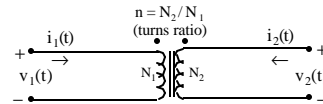
If the core is a *perfect* conductor of (magnetic) flux, there is no leakage flux and the transformer is ideal (see text pp. 124-126)



4.4 Ideal Transformer Properties

$$v_2(t) = n v_1(t) \text{ and } i_1(t) = -n i_2(t) \text{ so that ...}$$

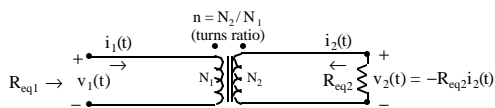
$$p_1(t) = v_1(t) i_1(t) = -v_2(t) i_2(t) = -p_2(t) \text{ (i.e., } p_1(t) + p_2(t) = 0 \text{ } \therefore \text{ lossless)}$$



4.4 Ideal Transformer Properties cont.

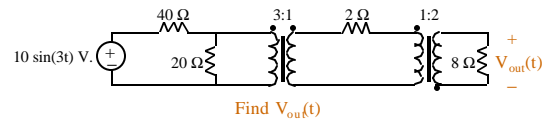
$$v_2(t) = n v_1(t) \text{ and } i_1(t) = -n i_2(t) \text{ so that ...}$$

$$R_{eq2} = -v_2(t)/i_2(t) = n^2(v_1(t)/i_1(t)) = n^2 R_{eq1} \text{ (i.e., reflected impedance concept—see text Example 4.6, pp. 127-128)}$$



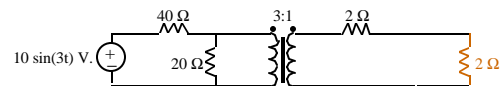
4.4 Ideal Transformer Properties cont.

Example 4-2 (Text Problem 4.25)



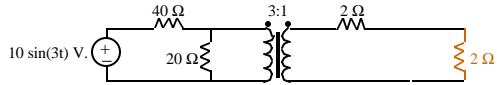
Solution

Begin by reflecting the 8Ω resistor across the right transformer using $R_{eq} = (1/2)^2 \times 8 \Omega = 2 \Omega$ to obtain the equivalent circuit below

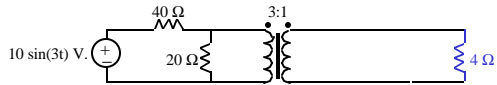


4.4 Ideal Transformer Properties cont.

Example 4-2 cont.

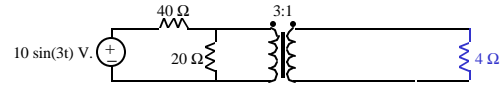


Next add the two series resistors ($2\ \Omega + 2\ \Omega = 4\ \Omega$) to obtain the circuit below

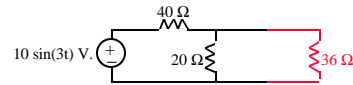


4.4 Ideal Transformer Properties cont.

Example 4-2 cont.

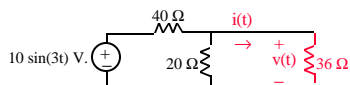


Reflect the $4\ \Omega$ resistor across the transformer using $R_{eq} = (3/1)^2 \times 4\ \Omega = 36\ \Omega$ to obtain the equivalent circuit below



4.4 Ideal Transformer Properties cont.

Example 4-2 cont.



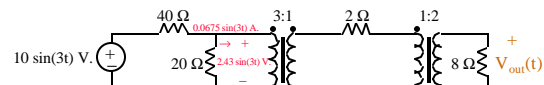
$20\ \Omega \parallel 36\ \Omega \approx 12.86\ \Omega$ along with voltage division yields:

$$v(t) \approx [12.86\ \Omega / (40 + 12.86)\ \Omega] \times 10\ \sin(3t)\ \text{V} \approx 2.43\ \sin(3t)\ \text{V}$$

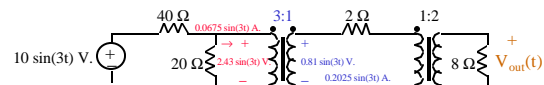
Then by Ohm's Law, $i(t) = v(t) / 36\ \Omega \approx 0.0675\ \sin(3t)\ \text{A}$.

4.4 Ideal Transformer Properties cont.

Example 4-2 cont.

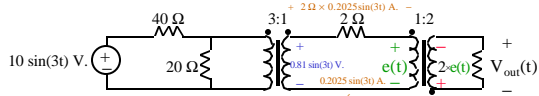


$v(t)$ and $i(t)$ as shown on the original schematic above, are next reflected to the right side of the left transformer as indicated below



4.4 Ideal Transformer Properties cont.

Example 4-2 cont.



KVL: $e(t) = 0.81\ \sin(3t)\ \text{V} - 2\ \Omega \times 0.2025\ \sin(3t)\ \text{A} \approx 0.405\ \sin(3t)\ \text{V}$.

As a result of the right transformer's turns ratio and the arrangement of its polarity dots (and KVL), the relationship between $V_{out}(t)$ and $e(t)$ is:

$$V_{out}(t) = -2 \times e(t)$$

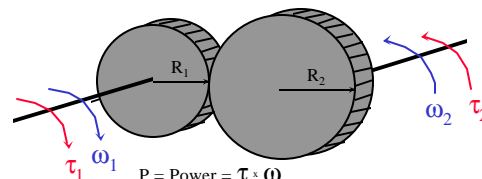
$$\therefore V_{out}(t) \approx -0.81\ \sin(3t)\ \text{V}$$

4.4 Ideal Transformer Properties cont.

Gear analogy

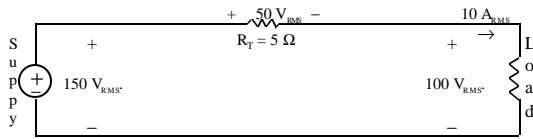
Transformer: $v_2(t) = n v_1(t)$ and $i_1(t) = -n i_2(t)$
where $n =$ coil turns ratio (N_2 / N_1)

Gears: $\tau_2(t) = -n \tau_1(t)$ and $\omega_1(t) = -n \omega_2(t)$
where $n =$ gear radii ratio (R_2 / R_1)



4.4 Power Transformer Application: Electric Power Transmission Application

Case 1: Load supplied directly from source via a transmission line



$$P_{\text{loss}} = (10 \text{ A}_{\text{RMS}})^2 \cdot 5 \Omega = 0.5 \text{ kW (Half the load's power!)}$$

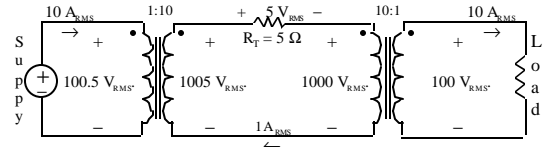
$$P_{\text{load}} = 100 \text{ V}_{\text{RMS}} \cdot 10 \text{ A}_{\text{RMS}} = 1 \text{ kW and } P_{\text{supply}} = 150 \text{ V}_{\text{RMS}} \cdot 10 \text{ A}_{\text{RMS}} = 1.5 \text{ kW}$$

$$\therefore \eta_{\%} = (1 \text{ kW} / 1.5 \text{ kW}) \cdot 100\% \approx 66.7\%$$

The voltage drop between the supply and the load is
 $150 \text{ V}_{\text{RMS}} - 100 \text{ V}_{\text{RMS}} = 50 \text{ V}_{\text{RMS}}$
 (A third of the supply's voltage!)

4.4 Power Transformer Application: Electric Power Transmission Application cont.

Case 2: Load supplied from source via a transmission line and transformers



$$P_{\text{loss}} = (1 \text{ A}_{\text{RMS}})^2 \cdot 5 \Omega = 5 \text{ W. (Only 0.5% of the load's power!)}$$

$$P_{\text{load}} = 1000 \text{ V}_{\text{RMS}} \cdot 1 \text{ A}_{\text{RMS}} = 1 \text{ kW and } P_{\text{supply}} = 100.5 \text{ V}_{\text{RMS}} \cdot 10 \text{ A}_{\text{RMS}} = 1.005 \text{ kW.}$$

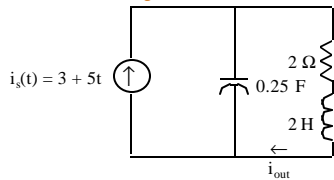
$$\therefore \eta_{\%} = (1 \text{ kW} / 1.005 \text{ kW}) \cdot 100\% \approx 99.5\%$$

The effective voltage drop between the supply and the load is
 $100.5 \text{ V}_{\text{RMS}} - 100 \text{ V}_{\text{RMS}} = 0.5 \text{ V}_{\text{RMS}}$
 (Only about 0.5% of the supply's voltage!)

4.5 Circuit Differential Equations

With the addition of energy storage elements, the formerly algebraic circuit equations become differential equations. For example, consider text Problem 4.33 (p. 143) whose schematic is shown below—where the objective is to get the differential equation relating i_{out} to i_s .

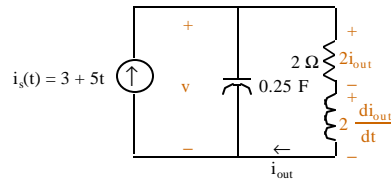
Example 4-3 (Text Problem 4.33)



4.5 Circuit Differential Equations cont.

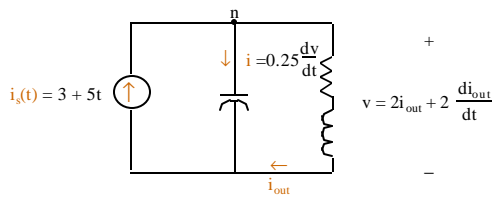
After writing the voltage-current relationships on the schematic as shown below, KVL yields ...

$$v = 2(di_{\text{out}}/dt) + 2i_{\text{out}}$$



4.5 Circuit Differential Equations cont.
 After writing the voltage-current relationship for the capacitor on the schematic as shown below, KCL at node n yields ...

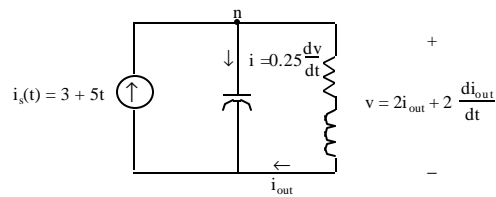
$$i_s(t) - i - i_{\text{out}} = 0 \text{ A. or ...}$$



4.5 Circuit Differential Equations cont.

$$i_s(t) - i - i_{\text{out}} = 0 \text{ A. or ...}$$

$$(3+5t) - 0.25 \frac{dv}{dt} - i_{\text{out}} = 0 \text{ A.}$$



4.5 Circuit Differential Equations cont.

$$(3+5t) - 0.25 \frac{dv}{dt} - i_{out} = 0 \text{ A.}$$

$$(3+5t) - 0.25 \frac{d}{dt} [2i_{out} + 2 \frac{di_{out}}{dt}] - i_{out} = 0 \text{ A.}$$

$$\frac{d^2 i_{out}}{dt^2} + \frac{di_{out}}{dt} + 2i_{out} = 6 + 10t$$

$i_s(t) = 3 + 5t$

$v = 2i_{out} + 2 \frac{di_{out}}{dt}$

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4.5 Circuit Differential Equations cont.

Example 4-4

Determine the differential equation relating $v(t)$ to $v_s(t)$.

The equivalent circuit is

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4.5 Circuit Differential Equations cont.

Example 4-4 cont.

KVL (left mesh): $v_s - 2 (di_1/dt) + di_2/dt = 0$ (A.)

KVL (right mesh): $di_1/dt - 3 di_2/dt - v = 0$ (B.)

Ohm's Law: $v = 0.5 \Omega \times i_2$ (C.)

Differentiate (C.) to obtain: $di_2/dt = 2 dv/dt$ (D.)

Substituting (D.) into (B.) yields: $di_1/dt = 6 dv/dt + v$ (E.)

Substituting (D.) & (E.) into (A.) yields:

$$dv/dt + 0.2 v = 0.1 v_s$$

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4.6 Energy Storage Elements Under DC Steady-State Conditions

Bottom Line

Under DC steady-state conditions, *inductors are short circuits and capacitors are open circuits*

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4.6 Energy Storage Elements Under DC Steady-State Conditions cont.

Example 4-5 (Text Problem 4.37, p. 144)

Find v_{out} under DC steady-state conditions

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4.6 Energy Storage Elements Under DC Steady-State Conditions cont.

With the capacitor opened and the inductor shorted, the current flowing down through the 1Ω resistor is 1 A . (by KCL at node n) so v_{out} (which is the resistor's voltage by KVL) is (by Ohm's Law) 1 V . $\therefore v_{out} = 1 \text{ V}$.

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**We welcome your
questions with
Enthusiasm!!**



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